

## **Job Description – Application Engineer**

Reports to: Engineering Manager

Salary: based upon knowledge and experience

## **Company**

EqcoLogic is a fabless semi-conductor company specializing in chips for high-speed communications applications. Products include Equalizers and Transceivers for data at between 100Mbit/s and 7Gbit/s.

## **Position Description:**

The Application Engineer will work together with the rest of the engineering team to design, debug and evaluate boards for high speed communication systems and to develop an automated system for device testing. Depending on his qualities and interests the Junior Engineer will be responsible for following functions:

- Board design

Design and layout of evaluation boards for in-house testing of EqcoLogic chips and demonstration boards for our customers.

- Board assembly

Assembly of prototype boards in small quantities, typically 2 or 3 boards.

- Debugging and testing boards

Debugging of prototype boards. Evaluation and testing of boards and chips in the lab. Writing measurement reports on obtained results.

- Device testing

Contributing to the development of automated systems for in-house testing of chips. This includes FPGA programming, software design, controlling the handler robot, processing of test data and documenting the test results.

## **Experience Description:**

- Relevant engineering degree essential.
- Prior working experience is not required.
- Experience with FPGAs, microcontroller programming, schematic capture and PCB design packages is a benefit.
- Good written and spoken English.